

Experience:

2006 - Tottori University, Japan

Associate Professor, Department of Electrical and Electronic Engineering, Faculty of Engineering

- Education of Electrical and Electronic Engineering
- Research on Digital Signal Processing and Its Application

2004 – 2006 Tottori University, Japan

Associate Professor, Department of Regional Environment, Faculty of Regional Sciences

- Education of Information Technology
- Research on Digital Signal Processing and Sound-scape

1999 – 2004 Tottori University, Japan

Associate Professor, Department of Regional Planning, Faculty of Education and Regional Sciences

- Education of Information Technology
- Research on Digital Signal Processing and Sound-scape

1997 – 1999 Tottori University, Japan

Associate Professor, Department of Technology, Faculty of Education

- Education of Electrical Engineering and Information Processing
- Research on Digital Signal Processing

1992 – 1997 Tottori University, Japan

Assistant Professor, Department of Technology, Faculty of Education

- Education of Electrical Engineering and Information Processing
- Research on Digital Signal Processing

1986 – 1992 SANYO Electric CO., Ltd. Japan

Researcher, Central Research Institute

- Research on Information and Communication System

Education:

- Dr.E., 1997 Osaka Prefecture University, Japan
- M.E., 1986 Osaka Prefecture University, Japan
- B.E., 1984 Osaka Prefecture University, Japan

Technical Activities:**[Committee]**

- 2001 – 2002 Secretary of IEICE Technical Committee on Circuits and Systems
- 2002 - Member of IEICE Technical Committee on Signal Processing
- 2003 - Member of IEICE Technical Committee on Circuits and Systems
- 2003 – 2004 IEEE Hiroshima Section Membership Development Committee
- 2005- Member of IEEE Hiroshima Section Executive Committee

- 2005- Member of IEEE Hiroshima Section Student Committee
- 2005 - Member of IEICE Technical Committee on Biometrics Security Group
- 2005 - Reviewer of IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences

[International Conference]

- 1998 International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC'98) Session Chair
- 1999 International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC'99) Session Chair
- 2000 IEEE International Symposium on Intelligent Signal Processing and Communication Systems (ISPACS2000) Technical Committee Member, Special Session Organizer and Session Chair
- 2000 International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC2000) Session Chair
- 2002 International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC2002) Technical Committee Member
- 2003 International Symposium on Signal Processing, Information and Communication Systems (ISPACS2003) Technical Committee Member
- 2004 IEEE International Symposium on Communications and Information Technologies 2004 (ISCIT2004) Technical Program Committee Member and Session Chair
- The 47th IEEE International Midwest Symposium on Circuits and Systems (MWSCAS2004) Publication Co-Chair and Student Paper Contest Chair
- 2004 International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC2004) Session Chair
- 2005 IEEE International Symposium on Circuits and Systems (ISCAS2005) Session Chair
- 2005 International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC2005) Session Chair
- 2006 International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC2006) Program Committee Member and Session Chair
- 2006 International Symposium on Intelligent Signal Processing and Communication Systems (ISPACS2006) Local Arrangement Chair

[Publication]

- 2002 IEICE Trans. on Special Issue: Papers Selected from ITC-CSCC 2002, Associate Editor
- 2003 IEICE Trans. on Special Section: Information Security in Multi-media Society, Editor
- 2003 IEICE Trans. on Special Section on Applications and Implementations of Digital Signal Processing, Associate Editor
- 2004 IEICE Trans. on Special Section on Adaptive Filtering, Associate Editor
- 2004 IEICE Trans. on Special Issue: Recent Advances in Circuits and Systems, Associate Editor
- 2004 IEICE Trans. on Special Section: Adaptive Signal Processing and Its application, Associate Editor

- 2004 IEICE Trans. on Special Section: Signal Processing, Associate Editor
- 2005 IEICE Trans. on Special Section: Multidimensional Signal Processing and Its Application, Associate Editor
- 2005 IEICE Trans. on Special Issue: Papers Selected from ITC-CSCC 2005, Associate Editor
- 2006 IEICE Trans. on Special Section: Papers Selected from the 20th Symposium on Signal Processing, Associate Editor
- 2006 IEICE Trans. on Special Section: Multimedia and Mobile Signal Processing, Associate Editor
- 2007 IEICE Trans. on Special Section on Signal Processing for Audio and Visual Systems and Its Implementations, Associate Editor

Publication List:

[Journal Papers]

- 1) S. Yoneda, I. Nakanishi, I. Sasaki, and T. Kasai, "Switched-Capacitor DFT and IDFT Circuit," International Journal of ELECTORNICS, Vol.67, No.6, pp.839-851, Dec. 1989
- 2) M. Suzuki, I. Nakanishi, T. Yamamoto, and Y. Fukui, "Multidimensional Composite-Adaptive Algorithms and Their Applications (in Japanese)," IEICE Trans. A, Vol.J76-A, No.12, pp.1684-1693, Dec. 1993
- 3) I. Nakanishi, and Y. Fukui, "A New Adaptive Convergence Factor Algorithm with the Constant Damping Parameter," IEICE Trans. Fundamentals, Vol.E78-A, No.6, pp.649-655, Jun. 1995
- 4) I. Nakanishi, Y. Itoh, and Y. Fukui, "Performance Analysis and Improvement of the NACF Algorithm," IEICE Trans. Fundamentals, Vol.E79-A, No.8, pp.1246-1251, Aug. 1996
- 5) I. Nakanishi, Y. Itoh, and Y. Fukui, "Reduction of Computational Complexity in the IA Algorithm," IEICE Trans. Fundamentals, Vol.E79-A, No.11, pp.1918-1921, Nov. 1996
- 6) I. Nakanishi, Y. Itoh, and Y. Fukui, "Image Reduction Method Using MR Codes (in Japanese)," IEICE Trans. A, Vol.J80-D-II, No.3, pp.705-714, Mar. 1997
- 6') I. Nakanishi, Y. Itoh, and Y. Fukui, "An Image Reduction Method Using MR Codes," Systems and Computers in Japan, Vol.28, No.7, pp.1-10, Jul. 1997
- 7) I. Nakanishi, Y. Hamahashi, Y. Itoh, and Y. Fukui, "A New Structure of Frequency Domain Adaptive Filter with Composite Algorithm," IEICE Trans. Fundamentals, Vol.E81-A, No.4, pp.649-655, Apr. 1998
- 8) I. Nakanishi, Y. Hamahashi, Y. Itoh, and Y. Fukui, "Reduction of Computational Complexity in the Frequency Domain Adaptive Filter Using the Modified DFT Pair (in Japanese)," IEICE Trans. A, Vol.J82-A, No.6, pp.916-919, Jun. 1999
- 9) I. Nakanishi, Y. Itoh, and Y. Fukui, "Introduction of Orthonormal Transform into Neural Filter for Accelerating Convergence Speed," IEICE Trans. Fundamentals, vol.E83-A, no.2, pp.367-370, Feb. 2000
- 10) I. Nakanishi, N. Nishiguchi, Y. Itoh, and Y. Fukui, "On-Line Signature Verification Based on Subband Decomposition by DWT and Adaptive Signal Processing (in Japanese)," IEICE Trans. A, Vol.J87-A, No.6, pp.805-815, Jun. 2004
- 10') I. Nakanishi, N. Nishiguchi, Y. Itoh, and Y. Fukui, "On-Line Signature Verification Based on Subband Decomposition by DWT and Adaptive Signal Processing," Electronics and

- Communications in Japan, Part 3, vol. 88, No.6, pp.1-11, Jun. 2005
- 11) I. Nakanishi, H. Sakamoto, N. Nishiguchi, Y. Itoh, and Y. Fukui, "Multi-matcher On-line Signature Verification System in DWT Domain," IEICE Trans. Fundamentals, vol.E89-A, no.1, pp.178-185, Jan. 2006
 - 12) I. Nakanishi, Y. Nagata, T. Asakura, Y. Itoh, and Y. Fukui, "Speech Noise Reduction System Based on Frequency Domain ALE Using Windowed Modified DFT Pair," IEICE Trans. Fundamentals, vol.E89-A, no.4, pp.950-959, Apr. 2006
 - 13) I. Nakanishi, H. Sakamoto, N. Nishiguchi, Y. Itoh, and Y. Fukui, "DWT Domain On-Line Signature Verification Using the Pen-Movement Vector," IEICE Trans. Fundamentals, vol. E89-A, no.4, pp.1129-1131, Apr. 2006
 - 14) S. Urase, N. Nousou, Y. Maniwa, K. Fujimura, I. Nakanishi, and Y. Fukui, "Classification and Visualization of Acceleration Plethysmogram Using Self-Organizing Maps," IPSJ Journal, vol. 48, no. 11, pp. 3588-3598, Nov. 2007
 - 15) I. Nakanishi, H. Sakamoto, N. Nishiguchi, Y. Itoh, and Y. Fukui, "Threshold Equalization for On-Line Signature Verification," IEICE Trans. Fundamentals, vol. E91-A, no. 8, pp. 2244-2247, Aug. 2008
 - 16) I. Nakanishi, "Biometric Authentication Using Intra-Body Propagation Signal, (in Japanese)," IEICE Trans. A, vol. J92-A, no. 12, pp. 980-986, Dec. 2009

[International Conference Proceedings]

- 1) S. Yoneda, I. Nakanishi, I. Sasaki, and T. Kasai, "Non-Recursive Filter Type Switched-Capacitor DFT Circuit Suitable for Its Inverse one," Proc. of 1986 Joint Technical Conference on Circuits and Systems (JTC-CAS'86), pp.83-90, Oct. 1986
- 2) M. Suzuki, I. Nakanishi, T. Yamamoto, and Y. Fukui, "Composite-Adaptive Algorithms and Their Applications," Proc. of 1993 Joint Technical Conference on Circuits/Systems, Computers and Communications (JTC-CSCC'93), Vol.1, pp.442-447, Jul. 1993
- 3) I. Nakanishi and Y. Fukui, "Pattern Recognition Using Hierarchical Feature Type and Location," Proc. of 1993 International Joint Conference on Neural Networks (IJCNN'93), Vol.3, pp.2165-2168, Oct. 1993
- 4) I. Nakanishi and Y. Fukui, "Normalized Adaptive Convergence Factor Algorithm," Proc. of 1994 Joint Technical Conference on Circuits/Systems, Computers and Communications (JTC-CSCC'94), Vol.1, pp.270-275, Jul. 1994
- 5) I. Nakanishi, T. Yamamoto, and Y. Fukui, "A New Composite-Adaptive Algorithm with Reduced Computational Complexity," Proc. of 1994 IEEE Asia-Pacific Conference on Circuits and Systems (APCCAS'94), pp.364-369, Dec. 1994
- 6) I. Nakanishi, Y. Itoh, and Y. Fukui, "Biased Adaptive Convergence Factor Algorithm," Proc. of 1995 Joint Technical Conference on Circuits/Systems, Computers and Communications (JTC-CSCC'95), Vol.1, pp.730-733, Jul. 1995
- 7) I. Nakanishi, Y. Itoh, and Y. Fukui, "A New Adaptive Damped Convergence Factor Algorithm with a Bias Term," Proc. of 1996 IEEE International Symposium on Circuits and Systems (ISCAS'96), Vol.2, pp.97-100, May 1996
- 8) I. Nakanishi, Y. Itoh, and Y. Fukui, "A New Frequency Domain Adaptive Filter Using a Modified Discrete Fourier Transform Pair," Proc. of International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC'96), pp.824-827, Jul. 1996
- 9) I. Nakanishi, Y. Itoh, and Y. Fukui, "A Frequency Domain Adaptive FIR Filter," Proc. of 1996

- IEEE International Workshop on Intelligent Signal Processing and Communication Systems (ISPACS'96), pp.1605-1609, Nov. 1996
- 10) I. Nakanishi, Y. Itoh, and Y. Fukui, "A New Frequency Domain Composite Adaptive Filter," Proc. of 1997 IEEE International Symposium on Circuits and Systems (ISCAS'97), pp.2276-2279, Jun. 1997
 - 11) James Okello, I. Nakanishi, Y. Itoh, Y. Fukui, and M. Kobayashi, "An Adaptive IIR Digital Filter Based on Estimation of Allpass and Minimum- Phase System," Proc. of 1997 IEEE International Symposium on Circuits and Systems (ISCAS'97), pp.2313-2316, Jun. 1997
 - 12) Y. Hamahashi, I. Nakanishi, Y. Itoh, and Y. Fukui, "A Frequency Domain Composite Adaptive FIR Filter and Reduction Methods of Its Computational Complexity," Proc. of International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC'97), pp.291-294, Jul. 1997
 - 13) James Okello, I. Nakanishi, Y. Itoh, Y. Fukui, and M. Kobayashi, "A New Modified Variable Step Size for The LMS Algorithm," Proc. of 1998 IEEE International Symposium on Circuits and Systems (ISCAS'98), Vol.V, pp.170-173, Jun. 1998
 - 14) Y. Hamahashi, N. Tsuji, I. Nakanishi, Y. Itoh, and Y. Fukui, "Performance Improvement of Adaptive Filters Using MDFT Pair," Proc. of International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC'98), pp.1483-1486, Jul. 1998
 - 15) I. Nakanishi, Y. Hamahashi, Y. Itoh, and Y. Fukui, "Frequency Domain Adaptive FIR Filter with Improved Performance," Proc. of 1998 IEEE International Workshop on Intelligent Signal Processing and Communication Systems (ISPACS'98), pp.767-771, Nov. 1998
 - 16) I. Nakanishi, Y. Itoh, and Y. Fukui, "Transform Domain Neural Filters] : Proc. of 1999 IEEE International Symposium on Circuits and Systems(ISCAS'99), vol.V, pp.579-582, May 1999
 - 17) I. Nakanishi, Y. Hamahashi, Y. Itoh, and Y. Fukui, "Frequency Domain Adaptive Noise Reduction Using Modified DFT Pair," Proc. of International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC'99), pp.1370-1373, Jul. 1999
 - 18) I. Nakanishi, Y. Hamahashi, Y. Itoh, and Y. Fukui, "Fast Frequency Domain Adaptive Filter," Proc. of 1999 IEEE International Symposium on Intelligent Signal Processing and Communication Systems (ISPACS'99), pp.453-456, Dec. 1999
 - 19) I. Nakanishi, Y. Itoh, and Y. Fukui, "Accelerated Frequency Domain Adaptive Filter Using Modified DFT Pair and Its Application to Noise Canceling," Proc. of 2000 IEEE International Symposium on Circuits and Systems (ISCAS2000), vol.V, pp.361-364, May 2000
 - 20) I. Nakanishi, Y. Nakamura, Y. Itoh, and Y. Fukui, "Noise Cancellation System Based on Frequency Domain Adaptive Filter Using Modified DFT Pair," Proc. of International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC2000), pp.225-228, Jul. 2000
 - 21) I. Nakanishi, Y. Nakamura, Y. Itoh, and Y. Fukui, "A Noise Reduction System Based on Frequency Domain Adaptive Filter Using Modified DFT Pair," Proc. of International Workshop on Signal Processing Application and Technology (SPAT2000), pp.160-165, Oct. 2000
 - 22) I. Nakanishi, Y. Nakamura, Y. Itoh, and Y. Fukui, "Application of Frequency Domain Adaptive Filter Using Modified DFT Pair to Noise Reduction Systems," Proc. of 2000 IEEE

- International Symposium on Intelligent Signal Processing and Communication Systems (ISPACS2000), pp.250-255, Nov. 2000
- 23) I. Nakanishi, Y. Itoh, Y. Fukui, and K. Fujii, "Noise Reduction System Using Modified DFT Pair," Proc. of 2000 IEEE International Symposium on Circuits and Systems (ISCAS2001), vol.II, pp.9-12, May 2001
 - 24) I. Nakanishi, Y. Itoh, and Y. Fukui, "Noise Reduction System Based on Frequency Domain Adaptive Filter Using Modified DFT Pair," Proc. of 2000 IEEE International Symposium on Circuits and Systems (ISCAS2001), vol.II, pp.737-740, May 2001
 - 25) I. Nakanishi, Y. Itoh, and Y. Fukui, "Two-Dimensional Frequency Domain Adaptive Filter Using Modified DFT Pair," Proc. of International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC2001), vol.I, pp.584-587, Jul. 2001
 - 26) I. Nakanishi, N. Nishiguchi, Y. Itoh, and Y. Fukui, "On-line Signature Verification Method Using Adaptive Algorithm in Wavelet Transform Domain," Proc. of International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC2002), vol.I, pp.385-388, Jul. 2002
 - 27) I. Nakanishi, Y. Itoh, and Y. Fukui, "Frequency Domain Noise Reduction System Using Nonlinear Predictor," Proc. of 2002 IEEE International Symposium on Intelligent Signal Processing and Communication Systems (ISPACS2002), pp.187-191, Nov. 2002
 - 28) I. Nakanishi, N. Nishiguchi, Y. Itoh, and Y. Fukui, "On-Line Signature Verification Method Utilizing Feature Extraction Based on DWT," Proc. of 2003 IEEE International Symposium on Circuits and Systems (ISCAS2003), vol.IV, pp.73-76, May 2003
 - 29) I. Nakanishi, N. Nishiguchi, Y. Itoh, and Y. Fukui, "Feature Extraction and Discrimination Method Based on DWT for On-Line Signature Verification," Proc. of International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC2003), pp.1136-1139, Jul. 2003
 - 30) I. Nakanishi, N. Nishiguchi, Y. Itoh, and Y. Fukui, "On-Line Signature Verification Based on DWT and Adaptive Algorithm," Proc. of 2003 IEEE International Symposium on Intelligent Signal Processing and Communication Systems (ISPACS2003), pp.553-556, Dec. 2003
 - 31) I. Nakanishi, N. Nishiguchi, Y. Itoh, and Y. Fukui, "On-Line Signature Verification Method Based on Discrete Wavelet Transform and Adaptive Signal Processing," Proc. of 2003 Workshop on Multimodal User Authentication (MMUA2003), pp.207-214, Dec. 2003
 - 32) I. Nakanishi, T. Asakura, Y. Itoh, and Y. Fukui, "Speech Noise Reduction Based on Frequency Domain Adaptive Line Enhancer," Proc. of 2004 International Congress on Acoustics (ICA2004), vol.II, pp.1667- 1670, Apr. 2004
 - 33) I. Nakanishi, H. Sakamoto, N. Nishiguchi, Y. Itoh, and Y. Fukui, "DWT Domain On-Line Signature Verification Based on Moving Vector," Proc. of International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC2004), pp.7F3P-56-1-4, Jul. 2004
 - 34) I. Nakanishi, H. Sakamoto, N. Nishiguchi, and Y. Itoh, "DWT Domain On-Line Signature Verification Using Pen-movement Vector," Proc. of The 47th IEEE International Midwest Symposium on Circuits and Systems(MWSCAS), vol.II, pp.569-572, Jul. 2004
 - 35) I. Nakanishi, N. Nishiguchi, Y. Itoh and Y. Fukui, "On-Line Signature Verification Based on Discrete Wavelet Domain Adaptive Signal Processing," D.Zhang and A. Jain Eds., "Biometric Authentication" (Proc. of ICBA2004), pp.584-591, Jul. 2004
 - 36) I. Nakanishi, T. Asakura, Y. Itoh, and Y. Fukui, "Frequency Domain De-correlation

- Parameter in Speech Noise Reduction System Based on Frequency Domain Adaptive Line Enhancer,” Proc. of The 47th IEEE International Midwest Symposium on Circuits and Systems(MWSCAS), vol.II, pp.13-16, Jul. 2004
- 37) I. Nakanishi, H. Sakamoto, N. Nishiguchi, Y. Itoh, and Y. Fukui, “Multi-matcher On-line Signature Verification in DWT Domain,” Proc. of International Symposium on Communication and Information Technologies 2004 (ISCIT2004), pp.509-514, Oct. 2004
 - 38) I. Nakanishi, H. Sakamoto, Y. Itoh, and Y. Fukui, “Multi-matcher On-line Signature Verification System in DWT Domain,” Proc. of IEEE 2005 International Conference on Acoustics, Speech and Signal Processing (ICASSP2005), vol.II, pp.965-968, Mar. 2005
 - 39) I. Nakanishi, H. Sakamoto, Y. Itoh, and Y. Fukui, “DWT Domain Multi-matcher On-line Signature Verification System,” Proc. of IEEE 2005 International Symposium on Circuits and Systems(ISCAS2005), pp.5413-5416, May 2005
 - 40) I. Nakanishi, H. Sakamoto, N. Nishiguchi, Y. Itoh, and Y. Fukui, “Optimal User Weighting Fusion in DWT Domain On-line Signature Verification,” Proc. of IEEE 2005 International Symposium on Circuits and Systems(ISCAS2005), pp.1698-1701, May 2005
 - 41) H. Sakamoto, I. Nakanishi, H. Hara, Y. Itoh, and Y. Fukui, “DWT Domain On-Line Signature Verification Using Pen-movement Angle,” Proc. of International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC2005), pp.3-4, Jul. 2005
 - 42) I. Nakanishi, H. Sakamoto, Y. Itoh, and Y. Fukui, “Optimal User Weighting Fusion in DWT Domain On-Line Signature Verification,” T. Kanade, A. Jain, and N. Ratha (Eds.), AVBPA2005, LNCS3546, Springer-Verlag, Berlin Heidelberg, pp.758-766, Jul. 2005
 - 43) I. Nakanishi, T. Asakura, Y. Itoh, and Y. Fukui, “Speech Noise Reduction System Based on Frequency Domain ALE Using Modified DFT Pair,” Proc. of 2005 International Workshop on Acoustic Echo and Noise Control (IWAENC 2005), pp.137-140, Sep. 2005
 - 44) I. Nakanishi, Y. Nagata, T. Asakura, Y. Itoh, and Y. Fukui, “Single-Channel Speech Enhancement Based on Frequency Domain ALE,” Proc. of IEEE 2006 International Symposium on Circuits and Systems(ISCAS2006), pp.2541-2544, May 2006
 - 45) Y. Nagata, I. Nakanishi, T. Asakura, Y. Itoh, and Y. Fukui, “Performance Improvement of Noise Reduction System Using Modified DFT Pair,” Proc. of International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC2006), pp.365-368, Jul. 2006
 - 46) I. Nakanishi, H. Hara, H. Sakamoto, Y. Itoh, Y. Fukui, “Parameter Fusion in DWT Domain On-Line Signature Verification,” Proc. of 2006 IEEE International Symposium on Intelligent Signal Processing and Communication Systems (ISPACS2006), pp.395-398, Dec. 2006
 - 47) T. Nakagawa, I. Nakanishi, Y. Itho, Y. Fukui, “Multi-Modal Biometrics Authentication Using On-Line Signature and Voice Pitch,” Proc. of 2006 IEEE International Symposium on Intelligent Signal Processing and Communication Systems (ISPACS2006), pp.399-402, Dec. 2006
 - 48) C. Miyamoto, I. Nakanishi, H. Hideki, Y. Itoh, Y. Fukui, “Biometrics Authentication Based on Spectrum Features of Electroencephalogram(EEG),” Proc. of International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC2007), pp.141-142, Jul. 2007

- 49) Y. Yorikame, Y. Nishio, I. Nakanishi, Y. Itoh, and Y. Fukui, "Biometric Identity Verification Using Intra-Body Propagation Signal," Proc. of International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC2007), pp. 1059-1060, Jul. 2007
- 50) I. Nakanishi, Y. Yorikane, Y. Itoh, and Y. Fukui, "Biometric Identity Verification Using Intra-Body Propagation Signal," Proc. of 2007 Biometrics Symposium, Sep. 2007
- 51) S. Koike, I. Nakanishi, and Y. Itoh, "Score Level Fusion in DWT Domain On-Line Signature Verification," Proc. of International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC2008), pp. 669-672, Jul. 2008
- 52) S. Baba, C. Miyamoto, and I. Nakanishi, "Personal Authentication Using New Feature Vector of Brain Wave," Proc. of International Technical Conference on Circuits/Systems, Computers and Communications (ITC-CSCC2008), pp.673-676, Jul. 2008
- 53) S. Koike, I. Nakanishi, and Y. Itoh, "Threshold Equalization Methods for On-Line Signature Verification," Proc. of the 11th International Conference on Frontiers in Handwriting Recognition (ICFHR2008), Aug. 2008
- 54) C. Miyamoto, S. Baba, and I. Nakanishi, "Biometric Person Authentication Using New Spectral Features of Electroencephalogram (EEG)," Proc. of 2008 IEEE International Symposium on Intelligent Signal Processing and Communication Systems (ISPACS2008), pp.312-315, Mar. 2009
- 55) Y. Minato, I. Nakanishi, "Noise Reduction System Using Signal and Noise Level Detectors in Frequency Domain," Proc. of 2008 IEEE International Symposium on Intelligent Signal Processing and Communication Systems (ISPACS2008), pp.180-183, Dec. 2009
- 56) Nakanishi, C. Miyamoto, and S. Baba, "EEG Based Biometric Authentication Using New Spectral Features," Proc. of 2009 IEEE International Symposium on Intelligent Signal Processing and Communication Systems (ISPACS2009), pp. 651-654, Dec. 2009
- 57) I. Nakanishi, K. Fujii, "Speech Noise Reduction Using Sequential Spectrum Detection Based On Modified DFT Pair," Proc. of 2009 IEEE International Symposium on Intelligent Signal Processing and Communication Systems (ISPACS2009), pp. 65-68, Dec. 2009